<u>S/N 10/643,741</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Steven L. Scott et al.

Examiner: Arpan P. Savla

Serial No.:

10/643,741

Group Art Unit: 2185

Filed:

August 18, 2003

Docket: 1376.733US1

Title:

MULTISTREAM PROCESSING MEMORY-AND BARRIER-

SYNCHRONIZATION METHOD AND APPARATUS

COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS RCE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

Serial/Patent No.	Filing Date/Issue Date	Attorney Docket	<u>Title</u>
10643577	August 18, 2003	1376.711US1	SYSTEM AND METHOD FOR PROCESSING MEMORY INSTRUCTIONS
10643738	August 18, 2003	1376.726US1	SYSTEM AND METHOD FOR PERFORMING TRANSLATION LOOKASIDE BUFFER OPERATIONS
10643574 7366873	August 18, 2003	1376.730US1	INDIRECTLY ADDRESSED VECTOR LOAD-OPERATE-STORE METHOD AND APPARATUS
10643727	August 18, 2003	1376.731US1	METHOD AND APPARATUS FOR INDIRECTLY ADDRESSED VECTOR LOAD-ADD-STORE ACROSS MULTI- PROCESSORS
11771931	June 29, 2007	1376.731US2	METHOD AND APPARATUS FOR INDIRECTLY ADDRESSED VECTOR LOAD-ADD-STORE ACROSS MULTI-PROCESSORS

Dkt: 1376.733US1

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,

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Date May 9, 2008

Reg. No. 35,075

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 9th day of May, 2008.

CANDIS BUENDING

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